

1 In the Claims

2 1—24. Cancel.

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4 25. (Currently Amended) A processor-implemented method for printing
5 and then optically scanning a test pattern, comprising:

6 determining a size of a print medium upon which the test pattern is to be
7 printed;

8 configuring the test pattern to include as many color ramps as will fit per
9 row based on the size of the print medium, wherein the color ramps on each row
10 are bounded on left and right sides by wide mark patches, wherein height of the
11 test pattern is increased in response to availability of color ramps beyond which
12 will fit in a row on the print medium, and wherein the color ramps are arrayed
13 horizontally along a width of the print medium when the size of the print medium
14 allows, and are arrayed vertically when the size of the print medium requires such
15 that cyan color ramps are put on a row apart from a row comprising other color
16 ramps of other colors; and

17 printing the test pattern on the print medium-medium; and
18 optically scanning the printed test pattern, wherein the optical scanning
19 comprises:

20 locating wide mark patches using an optical sensor by performing a
21 media movement in an X-axis direction;

22 rapidly scanning the color ramps to determine if a distance between
23 the left and right wide mark patches is as expected; and
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scanning more slowly in both left and right directions to obtain a densitometric value for ink density of each color ramp;

wherein scanning more slowly is performed with an amber LED when scanning cyan color ramps and a blue LED when scanning color ramps of other colors; and

whercin scanning a blank area on an approach to the cyan color ramps put on the row apart from color ramps of other colors is performed more rapidly than scanning the cyan color ramp.

26. (Cancel)

27. (Previously Presented) The processor-implemented method of claim 25, wherein configuring the test pattern comprises:

configuring the test pattern to include a second row only when space does not exist on a first row to add an additional color ramp.

28. (Cancel)

29. (Cancel)

30. (Previously Presented) The processor-implemented method of claim 25, wherein configuring the test pattern comprises:

moving the color ramps relative to each other to maximize width of the test pattern and minimize height of the test pattern.

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2 **31.** (Previously Presented) The processor-implemented method of claim
3 25, wherein configuring the test pattern comprises:

4 adjusting a relative position of the color ramps, between locating the color
5 ramps on a same row and locating the color ramps on two different rows, based
6 upon size of the color ramps and space available in the same row.
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9 **32.** (Previously Presented) The processor-implemented method of claim
10 25, wherein as many color ramps as will fit per row is based on a size of the color
11 ramps and a width of the print medium.

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13 **33.—44.** (Cancel)
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